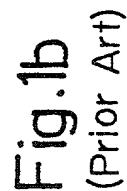


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METHOD OF MANUFACTURING AN INTEGRATED SEMICONDUCTOR
DEVICE HAVING A PLURALITY OF CONNECTION LEVELS

Inventor: Federico Pio

Express Mail No. EL897867915US

Docket No.: 854063.512D1

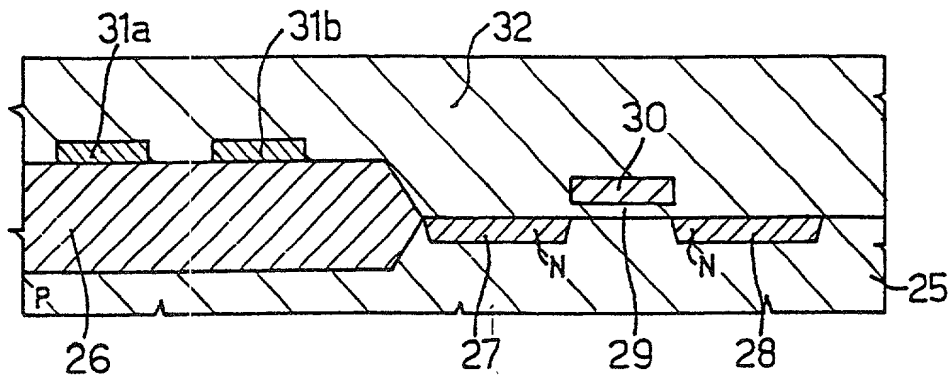


Fig. 3

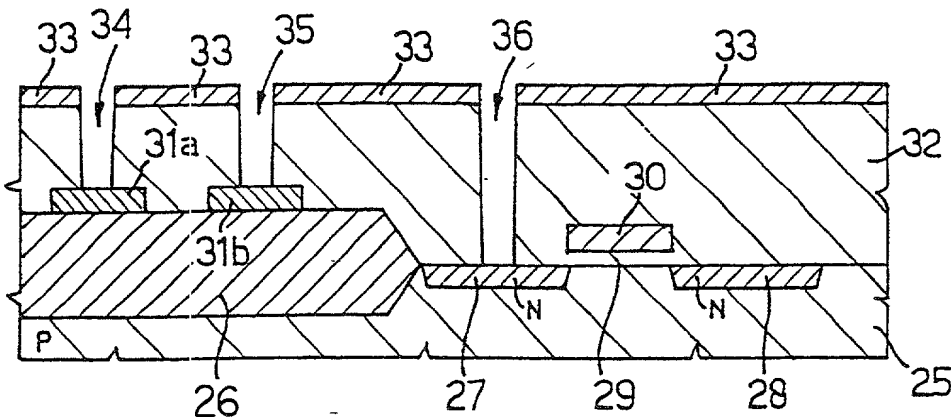


Fig. 4

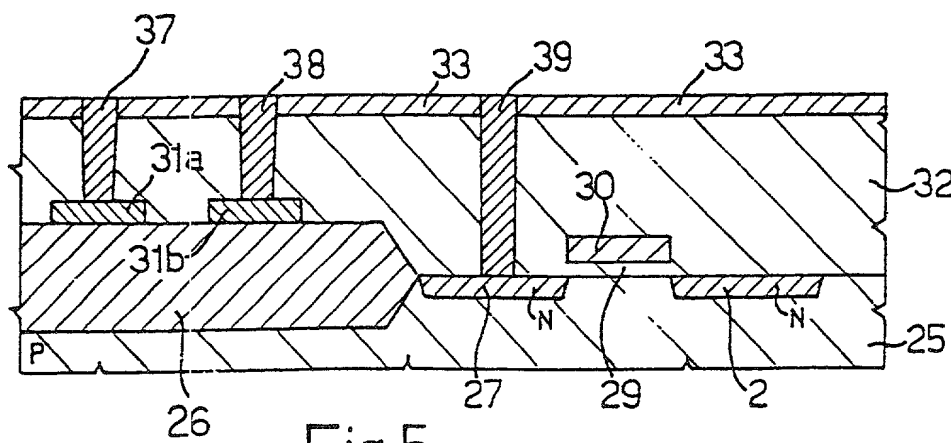


Fig. 5

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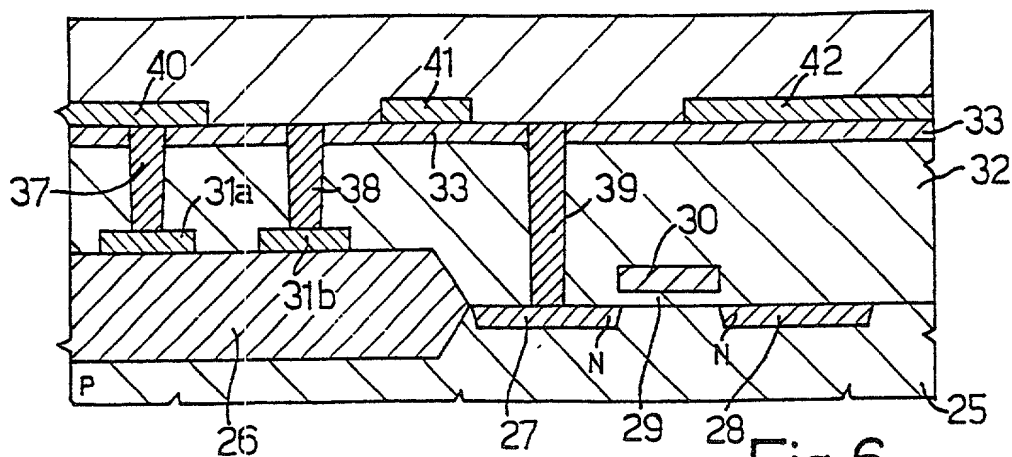


Fig. 6

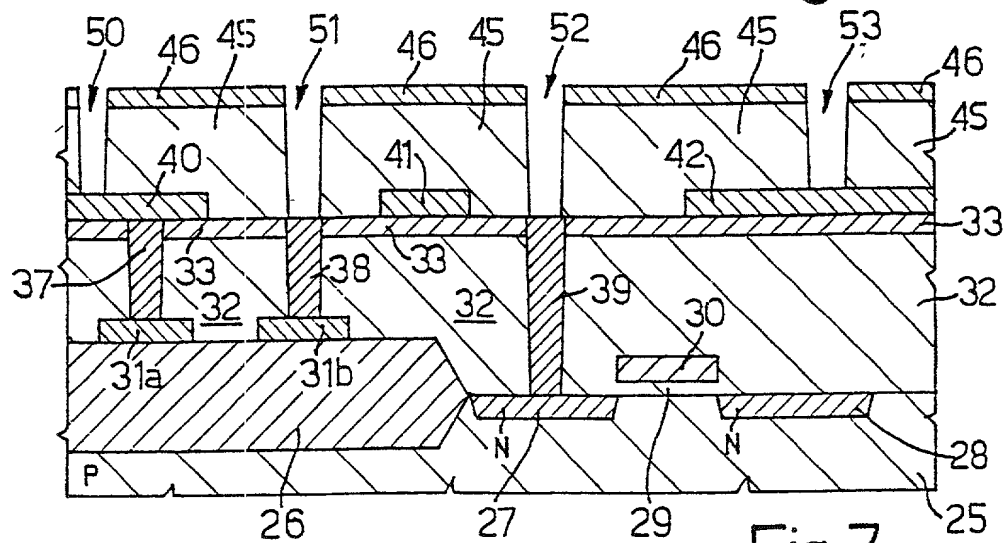


Fig. 7

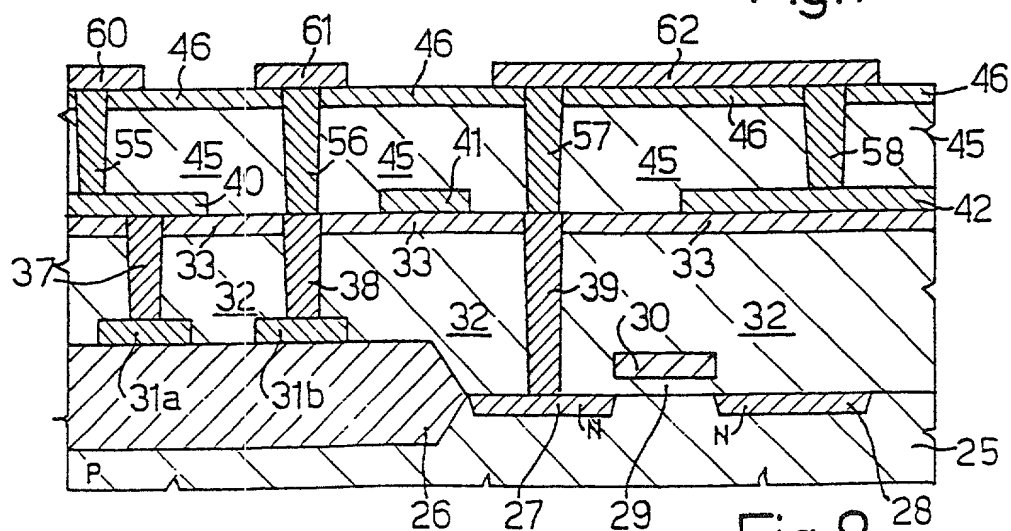


Fig.8

METHOD OF MANUFACTURING AN INTEGRATED SEMICONDUCTOR
 DEVICE HAVING A PLURALITY OF CONNECTION LEVELS

Inventor: Federico Pio

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Docket No.: 854063.512D1

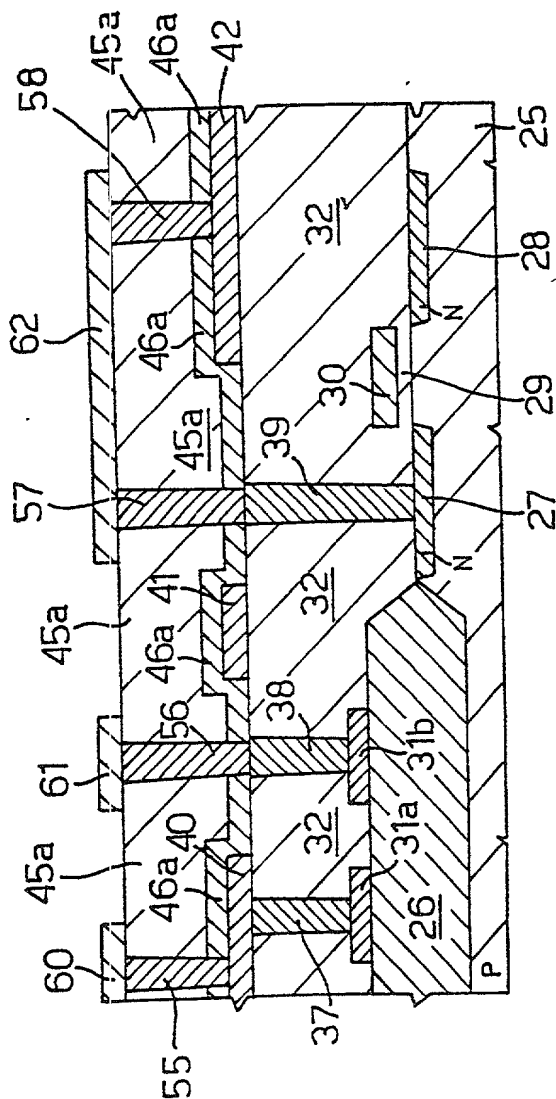


Fig. 9